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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Oleg GRUDIN et al.

Serial No.:

unknown (national stage of PCT/CA2004/000397)

International Filing Date:

March 19, 2004

Title:

TRIMMING TEMPERATURE COEFFICIENTS OF

ELECTRONIC COMPONENTS AND CIRCUITS

Agent of Record:

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Mail Stop Amendment

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450 U.S.A.

INFORMATION DISCLOSURE STATEMENT SUBMISSION OF REFERENCE CITED

Sir:

Pursuant to the duty of disclosure under 37 CFR 1.56, copies of the references listed on the attached PTO/SB/08A/B Form were submitted for the parent application.

In accordance with 37 CFR 1.97(h), the submission of the present information is not to be construed as an admission that such information is, or is considered to be material to patentability.

The Examiner is kindly requested to consider these references during the examination of the above-identified application, making the references of record, and to return an initialed copy of the PTO/SB/08A/B Form to the below-signed agent.

Respectfully submitted,

Oleg GRUDIN et al.

By:

Alexandra Daoud

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Enclosures

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				Application Number	unknown	
INF	ORMATION	I DIS	CLOSURE	Filing Date	unknown	
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l				Art Unit	unknown	
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Sheet	1	of	2	Attorney Docket Number	14836-10US AD/tf	

	U.S. PATENT DOCUMENTS						
Examiner Initials*	Cite No.1	Document Number Number - Kind Code ² (if known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant of Cited Document	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear		
		US 2002/008302	01-24-2002	HARRINGTON Thomas E. et al.			
			-				

	FOREIGN PATENT DOCUMENTS							
Examiner Initials*	Cite No.1	Foreign Patent Document Country Code3 – Number4 – Kind Code5 (# known)	Publication Date MM-DD-YYYY	Name of Patentee or Applicant Of Cited Document	Pages, Columns, Lines Where Relevant Passages or Relevant Figures Appear	T ⁴		

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NON PATENT LITERATURE DOCUMENTS				
Examiner Initials*	Cite No.1	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published	T ²	
		BABCOCK, J.A.et al. Polysilicon resistor trimming for pachaged integrated circuits Electron Devices		
		Meeting, 1993. Technical Digest., p 247-250 U.S.A. 1993 (XP010118446)		
		BABCOCK ,J.A. et al. <u>Precision electrical trimming of very low TCR poly-sige resistors</u> IEEE Electron Device Letters, IEEE Inc.,vol.21, no. 6, p283-285 U.S.A. 2000 (XP000951962)		
		FELDBAUMER D.W. et al. <u>Theory and aplpication of polsilicon resistor trimming</u> Solid State Electronics, Elsevier Science Publishers, vol. 38, no.11, p 1861-1869 G.B. 1995 (XP004002629)		

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